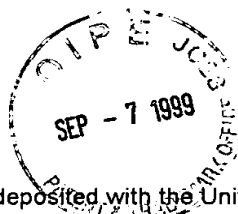


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By: 

Date: August 30, 1999

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Thomas Huttner et al.
Application No. : 09/313,424
Filed : May 17, 1999
Title : SOI Semiconductor Configuration And Method Of
Fabricating The Same
Art Unit : 2811

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

United States Patent No. 4,774,196 (Blanchard), dated September 27, 1988;

United States Patent No. 5,517,047 (Linn et al.), dated May 14, 1996;

German Published, Non-Prosecuted Patent Application DE 197 49 345 A1
(Iwamatsu et al.), dated August 27, 1998, semiconductor device and method for
producing it;

Japanese Patent Abstract No. 5-217822 (A), dated August 27, 1993;

Stephan Pindl et al.: "Silicon-on-insulator-Technologie: Neue "Low-Power"-CMOS-
Anwendungen mit betriebsspannungen kleiner 0,9 V", Phys. Bl. 54 91998) Nr. 4, pp.

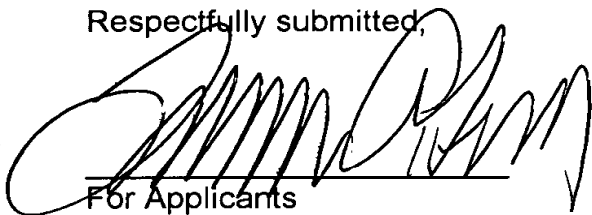
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353-355, silicon-on-insulator-technology: new "low-power"-CMOS-applications with an operating voltage of less than 0.9 V.

In accordance with 37 C.F.R. 1.97(e) the undersigned herewith states that each item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to the applicant.

Respectfully submitted,



For Applicants

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Date: August 30, 1999

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